

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	("5737631" "5136697"). pn.	US-PGPUB; USPAT	OR	ON	2008/01/10 18:36
S2	0	branch\$3 near3 execut\$3 near3 unit with own near5 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2007/07/17 11:58
S3	1	branch\$3 adj2 unit with own near5 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2007/07/18 14:47
S4	0	branch\$3 adj2 unit with dedicated near5 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2007/07/17 13:16
S5	6	unit with dedicated near5 control near3 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2007/07/17 13:17
S6	9	control with unit with dedicated near3 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2007/07/17 13:20
S7	13	predicate near3 unit with register near3 (file set)	US-PGPUB; USPAT	OR	ON	2007/07/17 13:21
S8	282	dedicated near3 "register file"	US-PGPUB; USPAT	OR	ON	2007/07/18 14:47
S9	11	dedicated near3 "register file" with (control predicate branch\$3)	US-PGPUB; USPAT	OR	ON	2007/07/17 13:22
S10	29	dedicated near3 "register file" with address	US-PGPUB; USPAT	OR	ON	2007/07/17 13:35
S11	1	branch near3 unit with (push\$3 pop\$4) with address with stack and stack with "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 13:33
S12	341	stack near3 "register file"	US-PGPUB; USPAT	OR	ON	2007/07/17 13:37
S13	27	stack near3 implement\$5 near3 "register file"	US-PGPUB; USPAT	OR	ON	2007/07/17 13:37
S14	1	"5964868".pn.	US-PGPUB; USPAT	OR	ON	2007/07/18 10:13
S15	1	(hull sharangpani) in. and v1w and stop near2 bit	US-PGPUB; USPAT	OR	ON	2007/07/18 14:27

S16	1	branch\$3 adj2 unit with own near5 register near3 (file set)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 14:47
S17	12	dedicated near3 "register file"	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/18 14:48
S19	314	712/24.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/18 14:48
S20	239	712/15.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/18 14:48
S21	328	712/22.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/18 14:48
S22	35	(hard-wired hardwired) with (section portion area) with FPGA	US-PGPUB; USPAT	OR	ON	2008/01/10 18:38
S23	0	(hard-wired hardwired) with (section portion area) with FPGA same (common \$2 frequent\$2)	US-PGPUB; USPAT	OR	ON	2008/01/10 18:39
S24	29	balko.in.	EPO	OR	ON	2008/01/10 18:46
S25	1	"20020089348".pn.	US-PGPUB; USPAT	OR	ON	2008/01/10 18:47
S26	2	("6,226,735" "6,255,849").pn.	US-PGPUB; USPAT	OR	ON	2008/01/10 18:48
S27	0	branch near3 unit with (push\$3 pop\$4) with "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 13:33
S28	63	((("register file" near3 stack) "register stack") with "return address"	US-PGPUB; USPAT	OR	ON	2008/01/14 13:34
S29	15	((("register file" near3 stack) "register stack") with "return address" same branch	US-PGPUB; USPAT	OR	ON	2008/01/14 13:34
S30	2	("5737631" "5136697").pn.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S31	0	branch\$3 near3 execut\$3 near3 unit with own near5 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S32	1	branch\$3 adj2 unit with own near5 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S33	0	branch\$3 adj2 unit with dedicated near5 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41

S34	6	unit with dedicated near5 control near3 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S35	9	control with unit with dedicated near3 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S36	16	predicate near3 unit with register near3 (file set)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S37	290	dedicated near3 "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S38	12	dedicated near3 "register file" with (control predicate branch\$3)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S39	30	dedicated near3 "register file" with address	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S40	1	branch near3 unit with (push\$3 pop\$4) with address with stack and stack with "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S41	353	stack near3 "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S42	27	stack near3 implement\$5 near3 "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S43	1	"5964868".pn.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S44	13	dedicated near3 "register file"	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/14 15:41
S45	1	(hull sharangpani).in. and vllw and stop near2 bit	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S46	1	branch\$3 adj2 unit with own near5 register near3 (file set)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/14 15:41
S47	333	712/24.ccls.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S48	345	712/22.ccls.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S49	244	712/15.ccls.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S50	29	balko.in.	EPO	OR	ON	2008/01/14 15:41
S51	35	(hard-wired hardwired) with (section portion area) with FPGA	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41

S52	0	(hard-wired hardwired) with (section portion area) with FPGA same (common \$2 frequent\$2)	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S53	1	"20020089348".pn.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S54	2	("6,226,735" "6,255,849").pn.	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S55	63	((("register file" near3 stack) "register stack") with "return address"	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S56	0	branch near3 unit with (push\$3 pop\$4) with "register file"	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S57	15	((("register file" near3 stack) "register stack") with "return address" same branch	US-PGPUB; USPAT	OR	ON	2008/01/14 15:41
S58	18	huisman.xp.	US-PGPUB; USPAT	OR	ON	2009/01/08 15:38
S59	8	fpga same (preconfigured pre-configured) same (multiply multipli\$6)	US-PGPUB; USPAT	OR	ON	2009/01/08 17:32
S60	2	("5754459" "6,362,650").pn.	US-PGPUB; USPAT	OR	ON	2009/01/08 17:34
S61	146	fpga same (predefined fixed) same (multiply multipli\$6)	US-PGPUB; USPAT	OR	ON	2009/01/08 17:38
S62	12	fpga same (predefined fixed) same (multiply multipli\$6) and programm \$4 with (connect\$3 interconnect\$3 coupl\$3) with (predefined fixed)	US-PGPUB; USPAT	OR	ON	2009/01/08 17:42
S63	153	fpga same (dedicated fixed) same (counter)	US-PGPUB; USPAT	OR	ON	2009/01/09 16:50
S64	13	fpga same (dedicated fixed) near3 (accumulator counter)	US-PGPUB; USPAT	OR	ON	2009/01/09 16:51
S65	427	fpga same (fixed dedicated) with (clock timer counter accumulator)	US-PGPUB; USPAT	OR	ON	2009/01/09 17:41
S66	43	fpga with (fixed dedicated) near3 (clock timer counter accumulator)	US-PGPUB; USPAT	OR	ON	2009/01/09 17:42
S67	6	fpga with (fixed dedicated) near3 (adder)	US-PGPUB; USPAT	OR	ON	2009/01/09 17:43

S68	8	fpga same (fixed dedicated) near3 carry with chain	US-PGPUB; USPAT	OR	ON	2009/01/09 17:55
S69	91	fpga same embedded with (multiplier multiplication adder carry chain counter)	US-PGPUB; USPAT	OR	ON	2009/01/09 17:56
S70	1453	326/39 ccls.	US-PGPUB; USPAT	OR	ON	2009/01/12 12:56
S71	3	fpga same embedded with (multiplier multiplication adder carry chain counter)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/12 12:57
S72	39	parallel with \$2configurable with fixed	US-PGPUB; USPAT	OR	ON	2009/08/25 18:08
S73	7	unit with dedicated near5 control near3 register near3 (file set)	US-PGPUB; USPAT	OR	ON	2009/08/25 18:10
S74	1	"6052773".pn.	US-PGPUB; USPAT	OR	ON	2009/08/25 18:33
S75	1	"6052773".pn.	US-PGPUB; USPAT	OR	ON	2010/01/29 15:42
S76	7	fpga with (fixed dedicated hardwired hard-wired) near3 (ALU adder)	US-PGPUB; USPAT	OR	ON	2010/02/01 12:05
S77	8	fpga with (fixed dedicated hardwired hard-wired) near3 (ALU adder)	US-PGPUB; USPAT	OR	ON	2010/07/12 10:29
S78	491	processor with accelerator and accelerator with (fpga \$2configurable)	US-PGPUB; USPAT	OR	ON	2010/07/12 10:31
S79	908	(configur\$5 reconfigur\$5) near3 instruction same (opcode operation function \$4) same operand	US-PGPUB; USPAT	OR	ON	2011/01/06 15:31
S80	209	(configur\$5 reconfigur\$5) near3 instruction with (opcode operation function \$4) with operand	US-PGPUB; USPAT	OR	ON	2011/01/06 15:31
S81	145	(configur\$5 reconfigur\$5) near3 instruction with (opcode operation function \$4) with operand and "712"/\$ ccls	US-PGPUB; USPAT	OR	ON	2011/01/06 15:31
S82	9	(configur\$5 reconfigur\$5) near3 instruction with opcode with (operation function\$4) with operand and "712"/\$ ccls.	US-PGPUB; USPAT	OR	ON	2011/01/06 15:31

S83	0	(reconfigur\$5) near3 instruction with opcode with (operation function \$4) with operand and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2011/01/06 15:32
S84	2	(reconfigur\$5) near3 instruction with (opcode operation function\$4) with operand and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2011/01/06 15:32
S85	0	reconfigur\$5 near3 instruction with (fpga hardware array) same (opcode operation function \$4) same operand and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2011/01/06 15:34
S86	89	reconfigur\$5 near3 instruction with (fpga hardware array) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2011/01/06 15:35
S87	70	reconfigur\$5 near2 instruction with (fpga hardware array) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2011/01/06 15:35
S88	2	("2002/0174266").URPN.	USPAT	OR	ON	2011/01/06 16:00
S89	3	("6438737" "6539438" "6289396").PN.	USPAT	OR	ON	2011/01/06 16:03

EAST Search History (Interference)

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1/6/11 6:26:38 PM

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